

FIG. 1

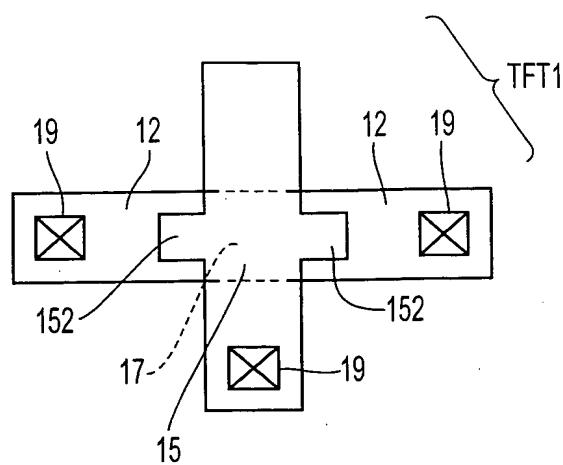


FIG. 2

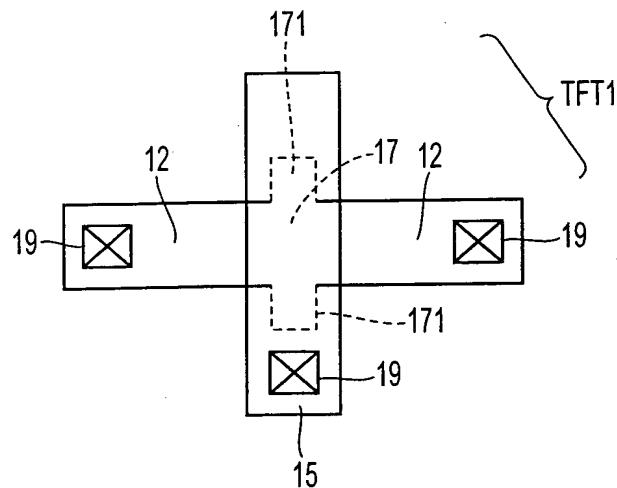


FIG. 3

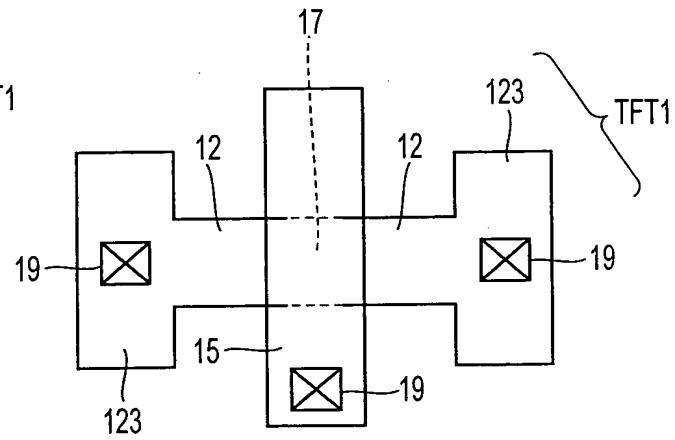


FIG. 4

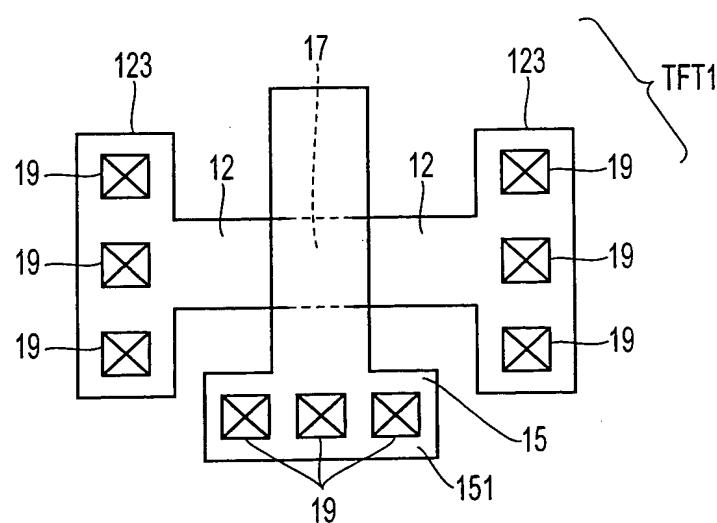


FIG. 5

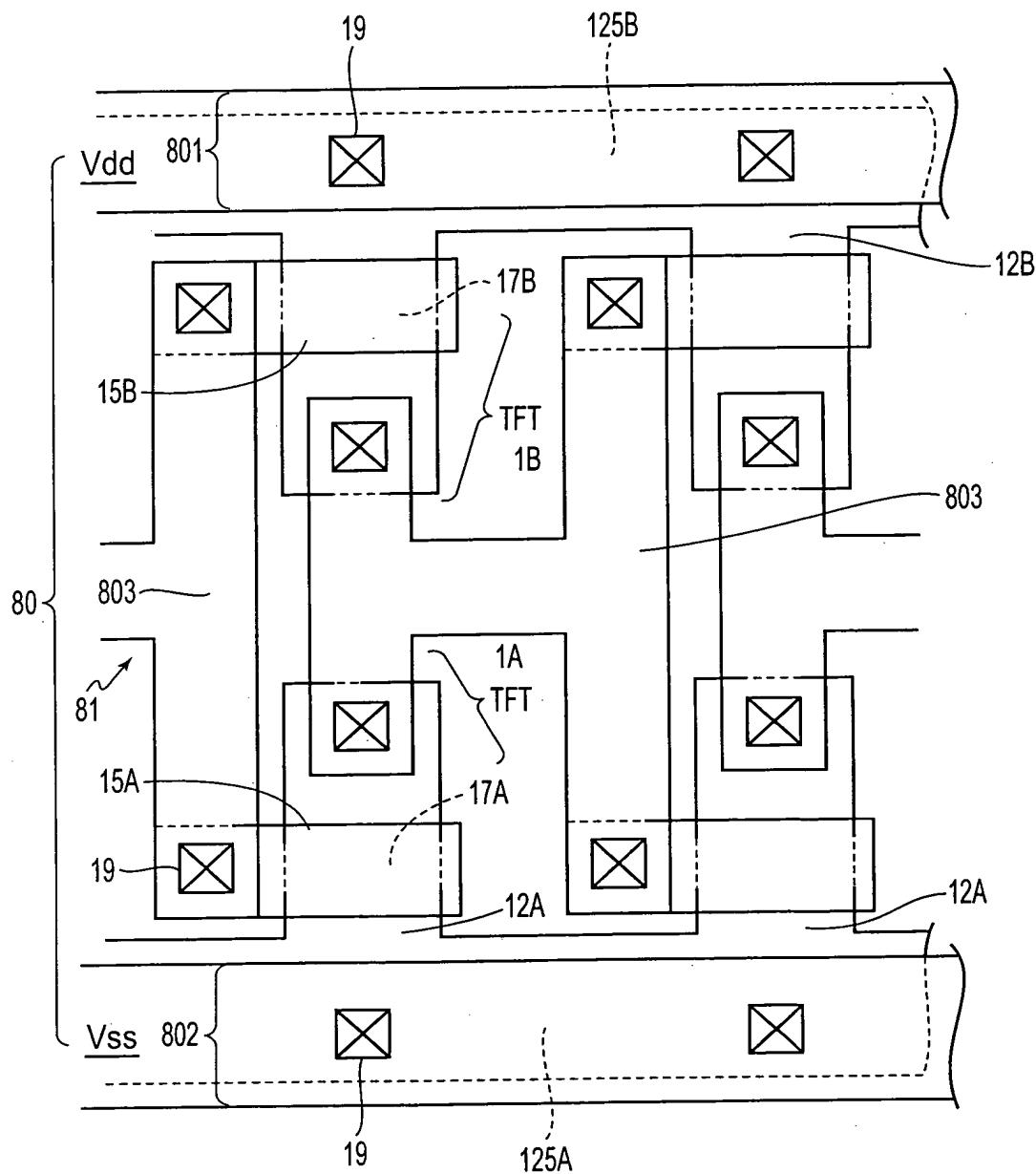


FIG. 6

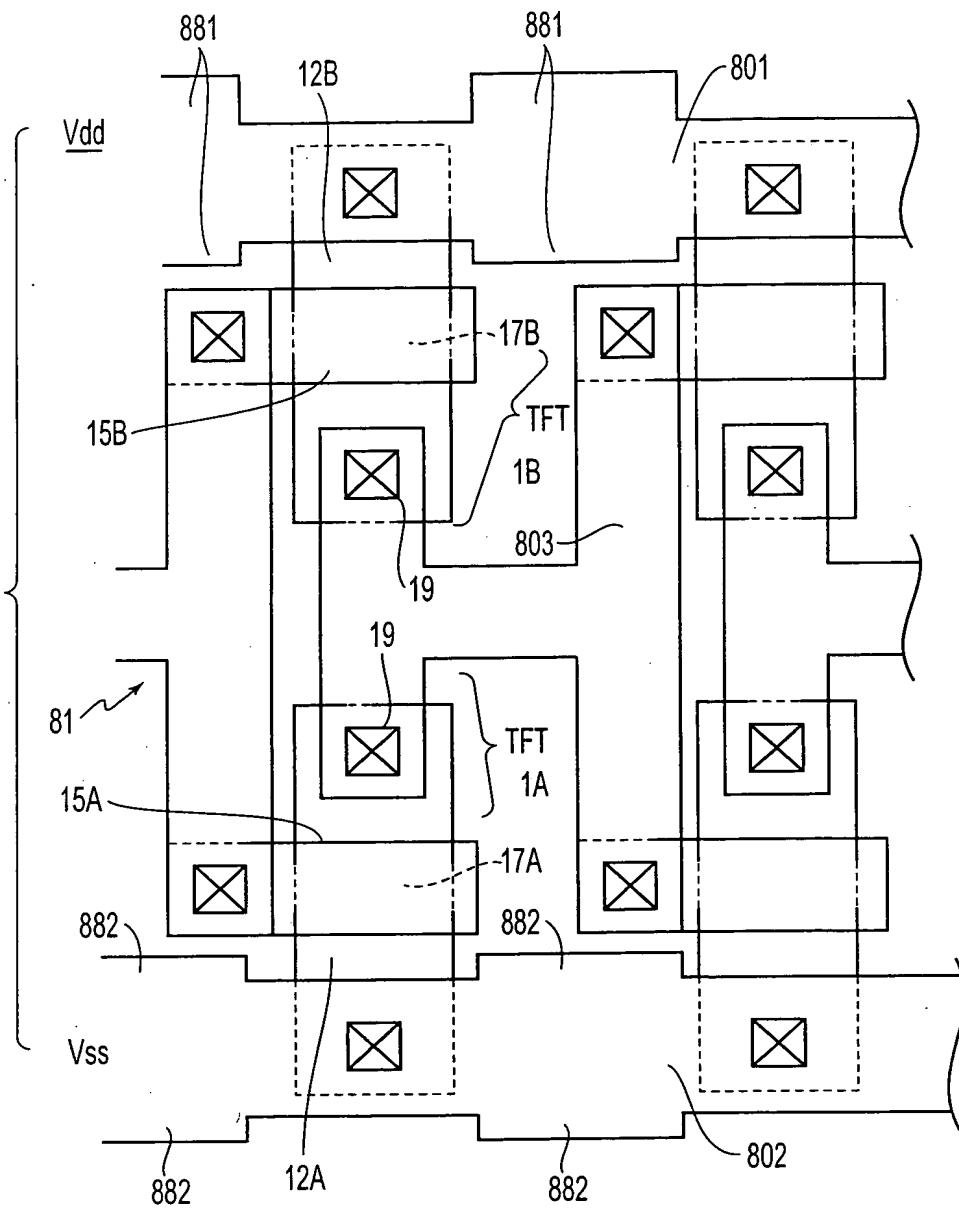


FIG. 7A

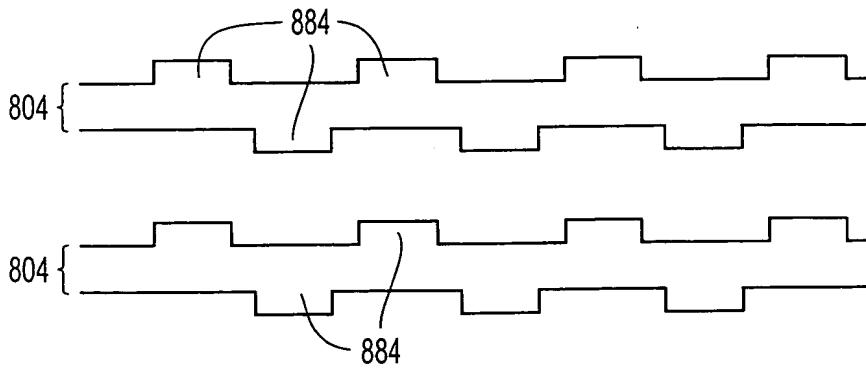


FIG. 7B

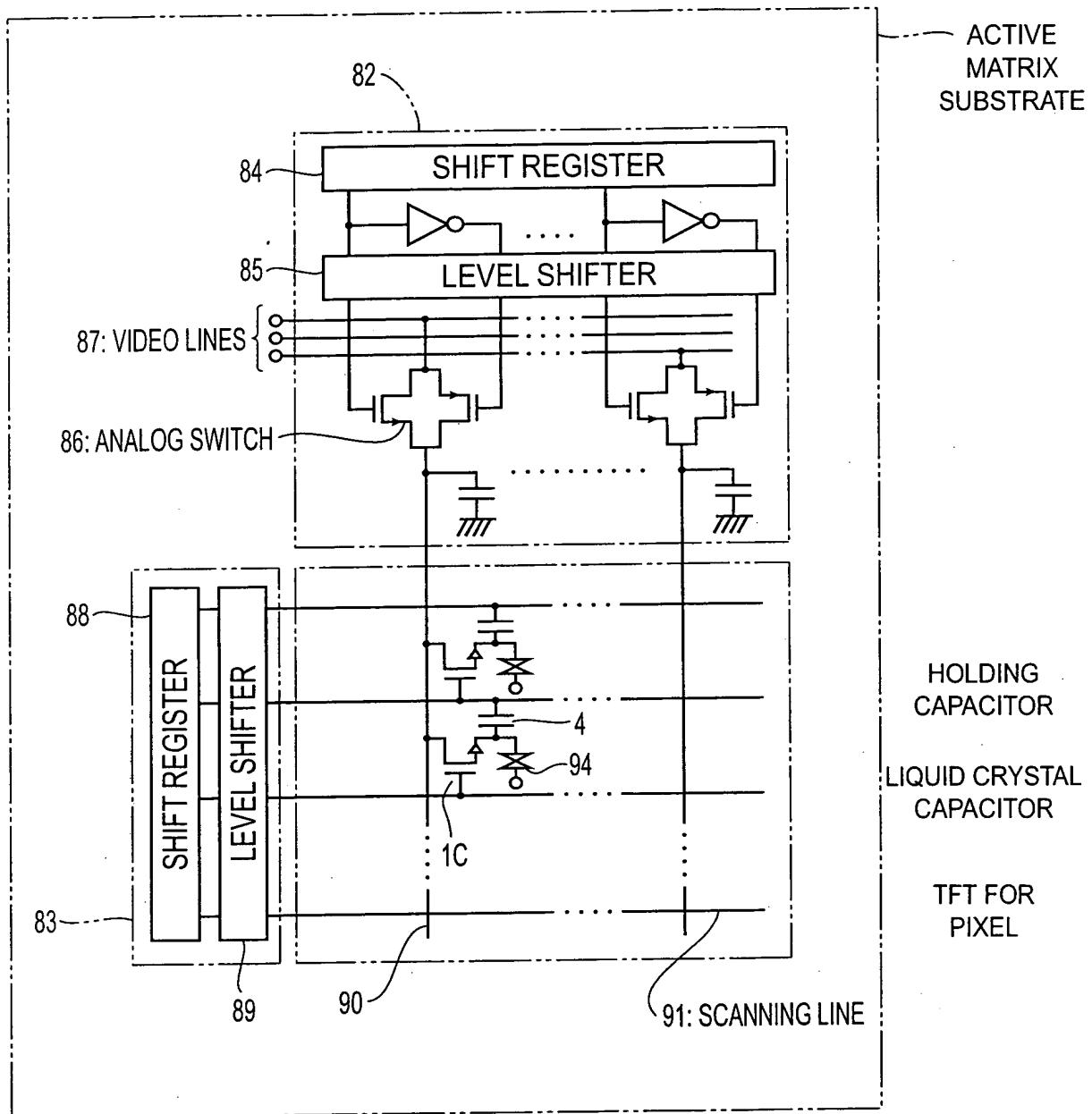


FIG. 8

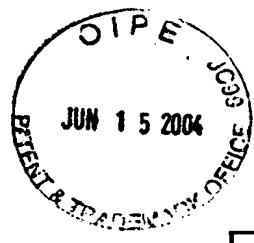


FIG. 9A

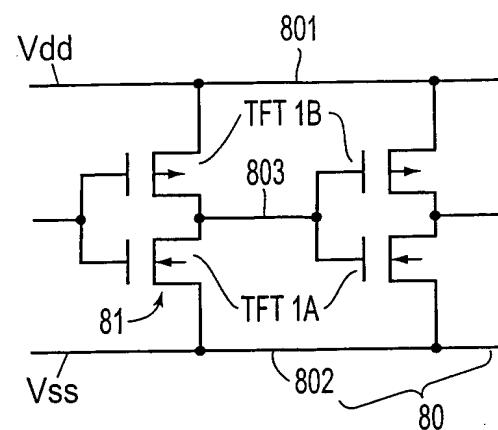
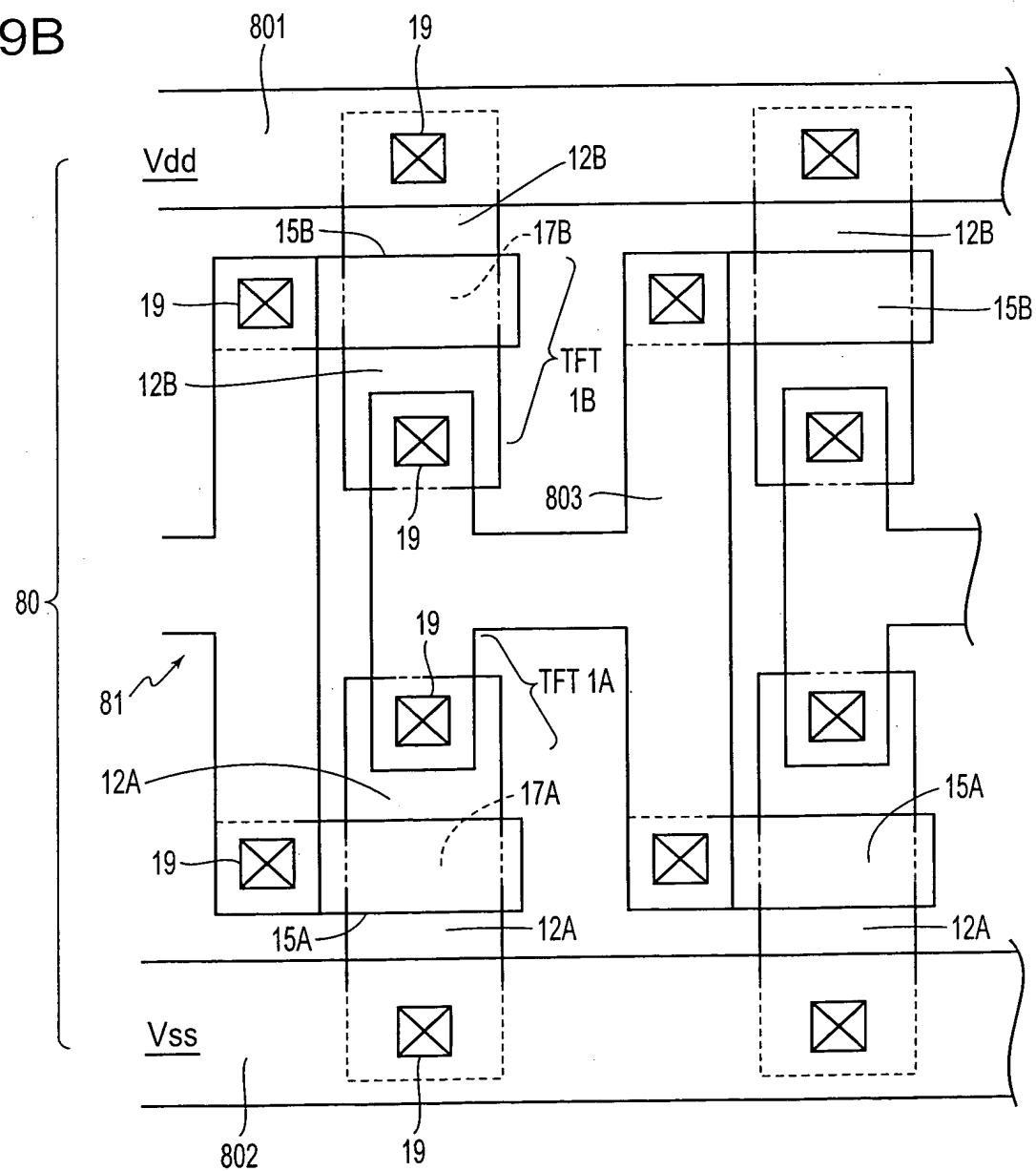


FIG. 9B



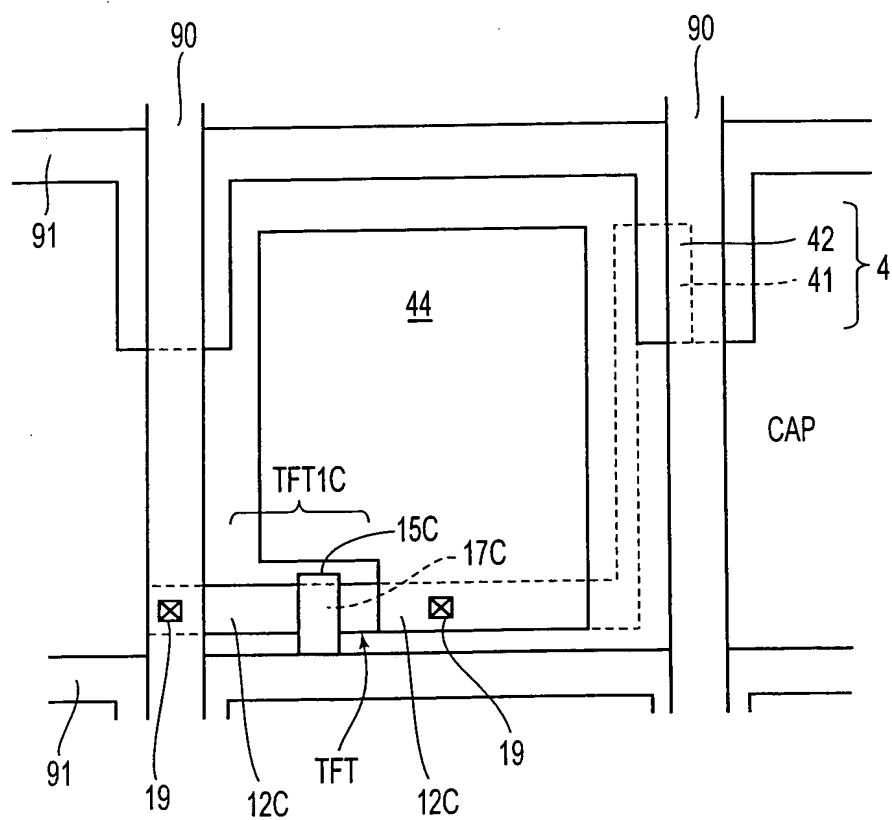


FIG. 10

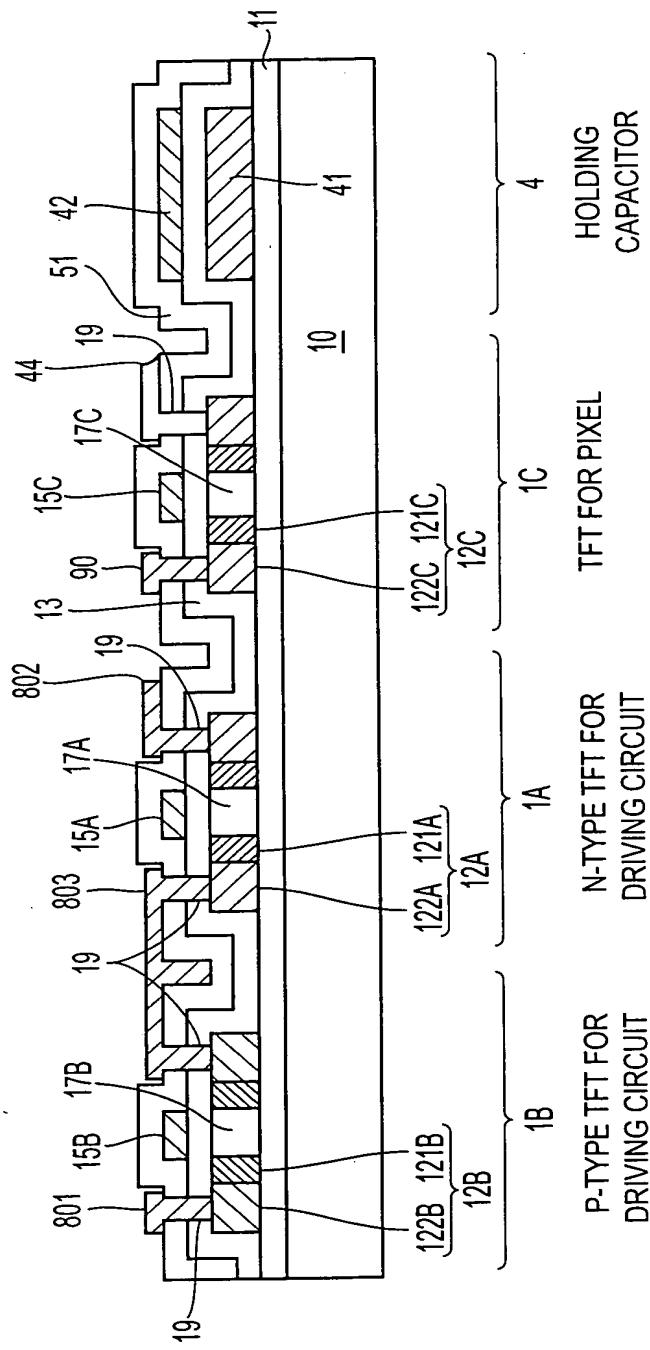




FIG. 12A

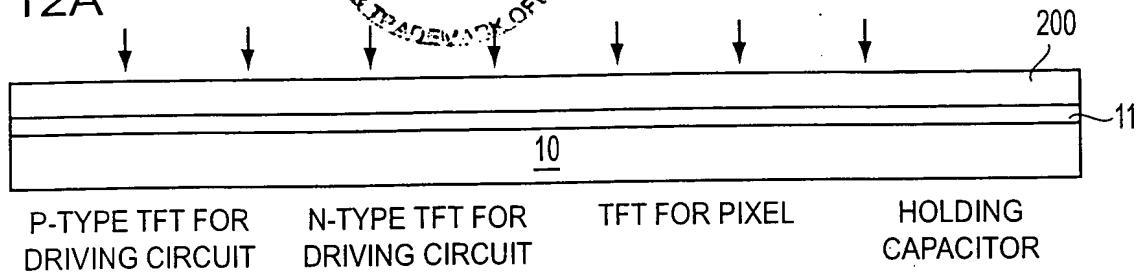


FIG. 12B

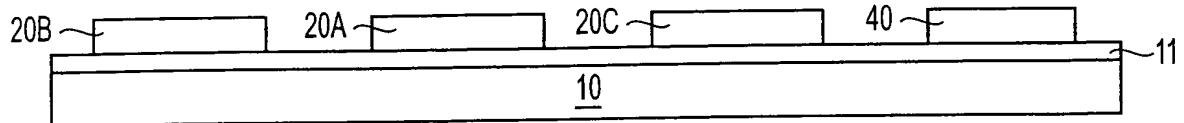


FIG. 12C

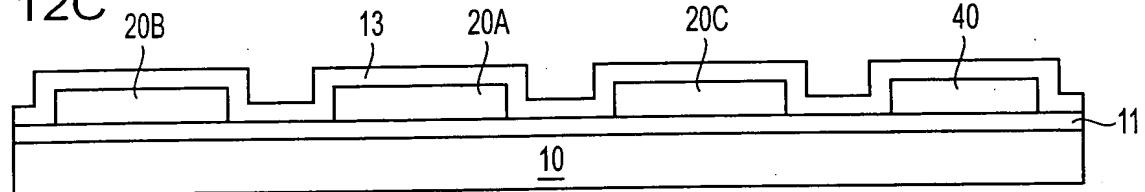


FIG. 12D

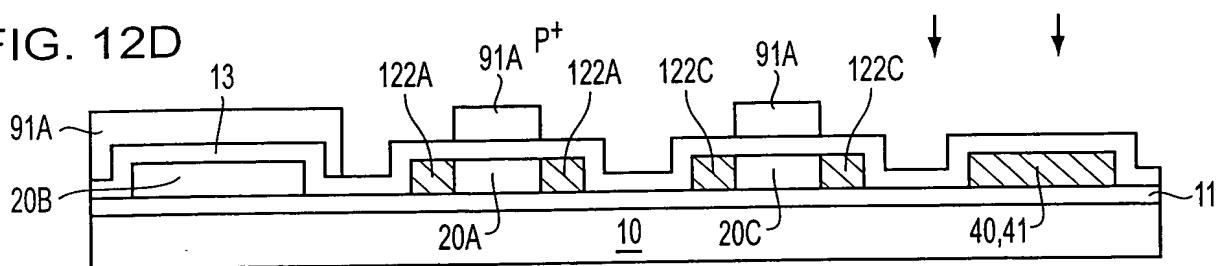


FIG. 12E

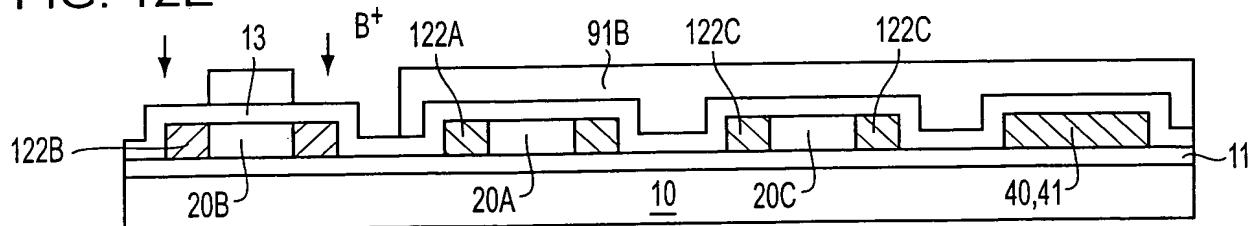


FIG. 12F

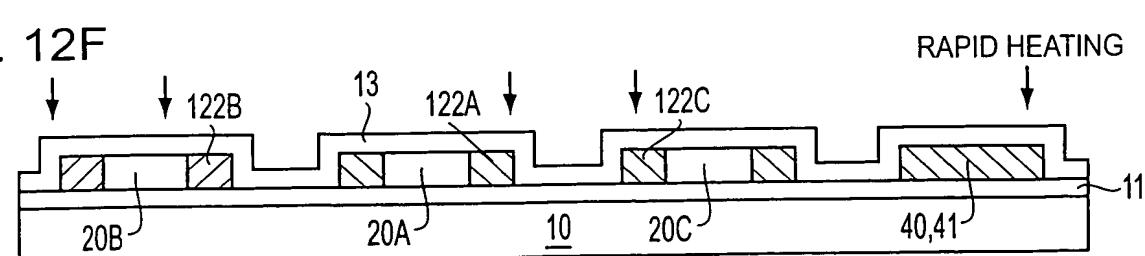


FIG. 13A

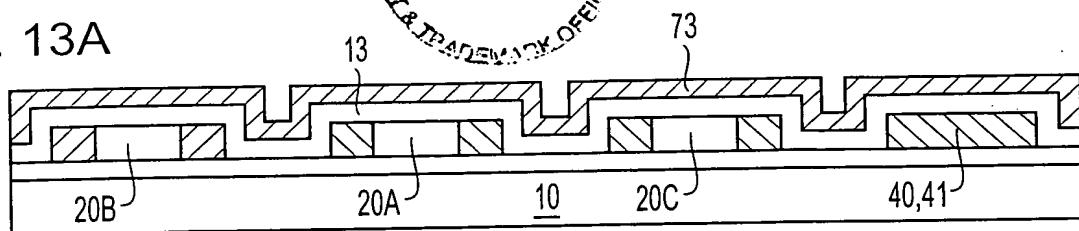


FIG. 13B

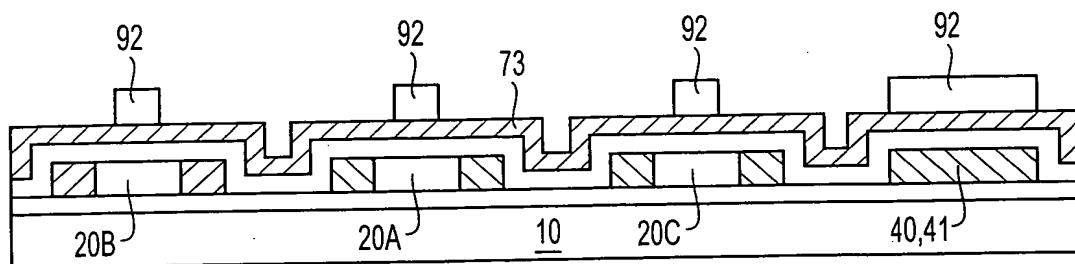


FIG. 13C

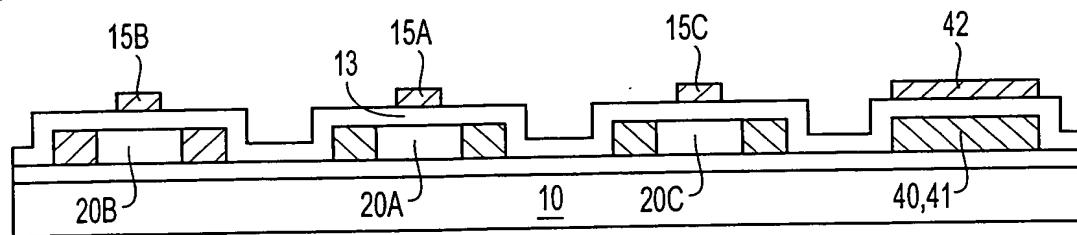


FIG. 13D

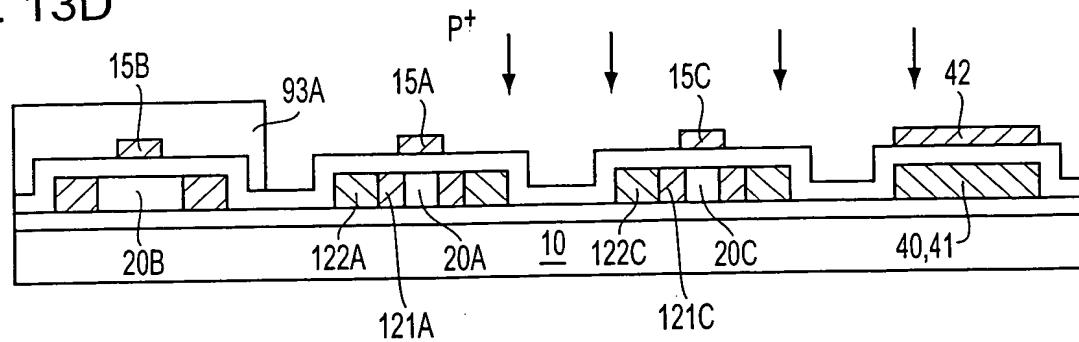




FIG. 13E

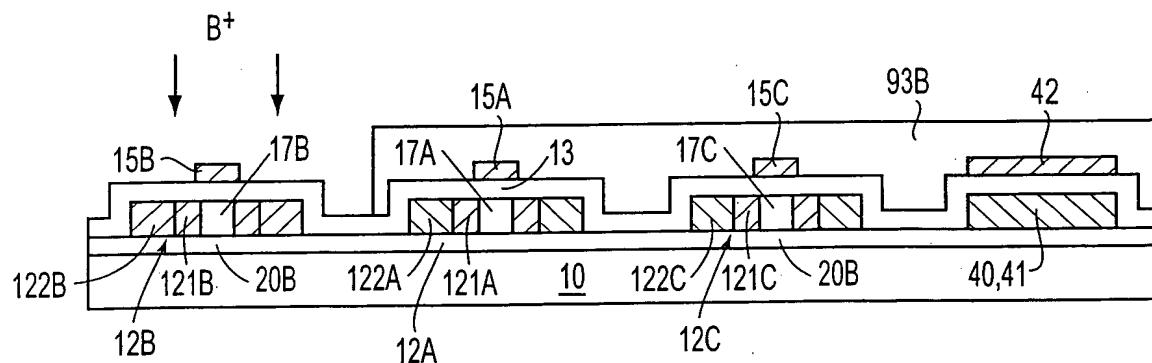
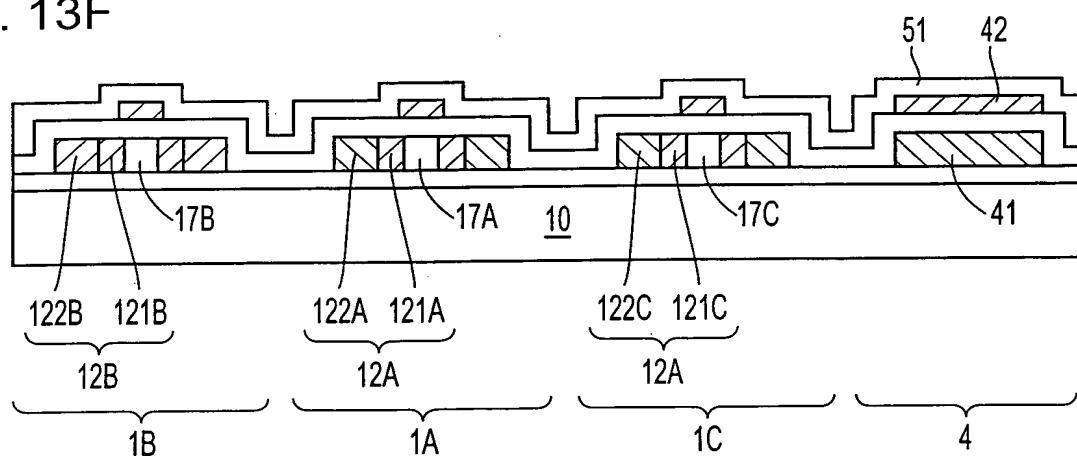


FIG. 13F



P-TYPE TFT FOR
DRIVING CIRCUIT

N-TYPE TFT FOR
DRIVING CIRCUIT

TFT FOR PIXEL

HOLDING
CAPACITOR

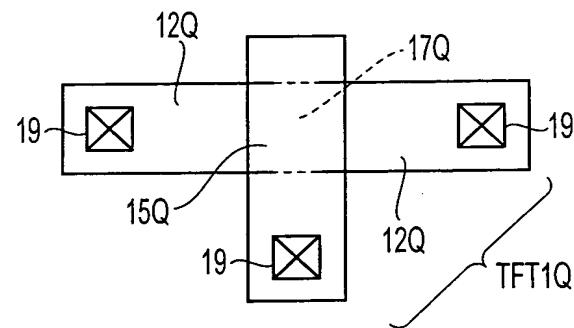


FIG. 14

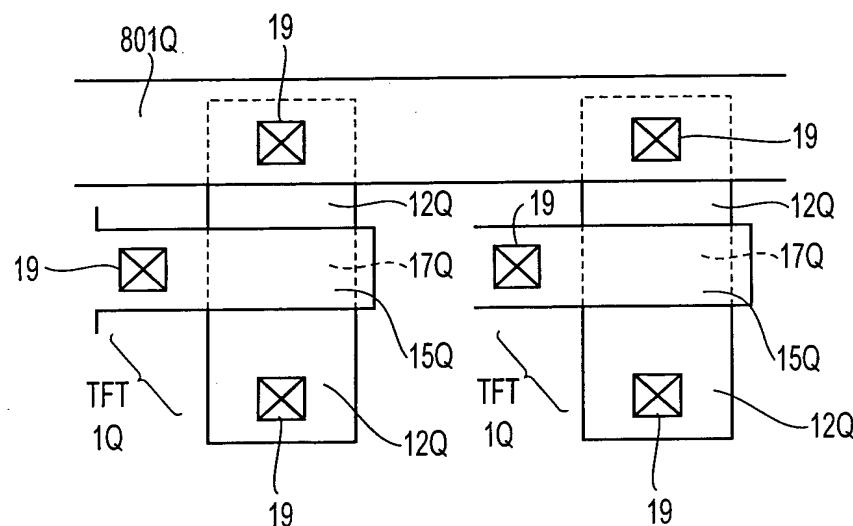
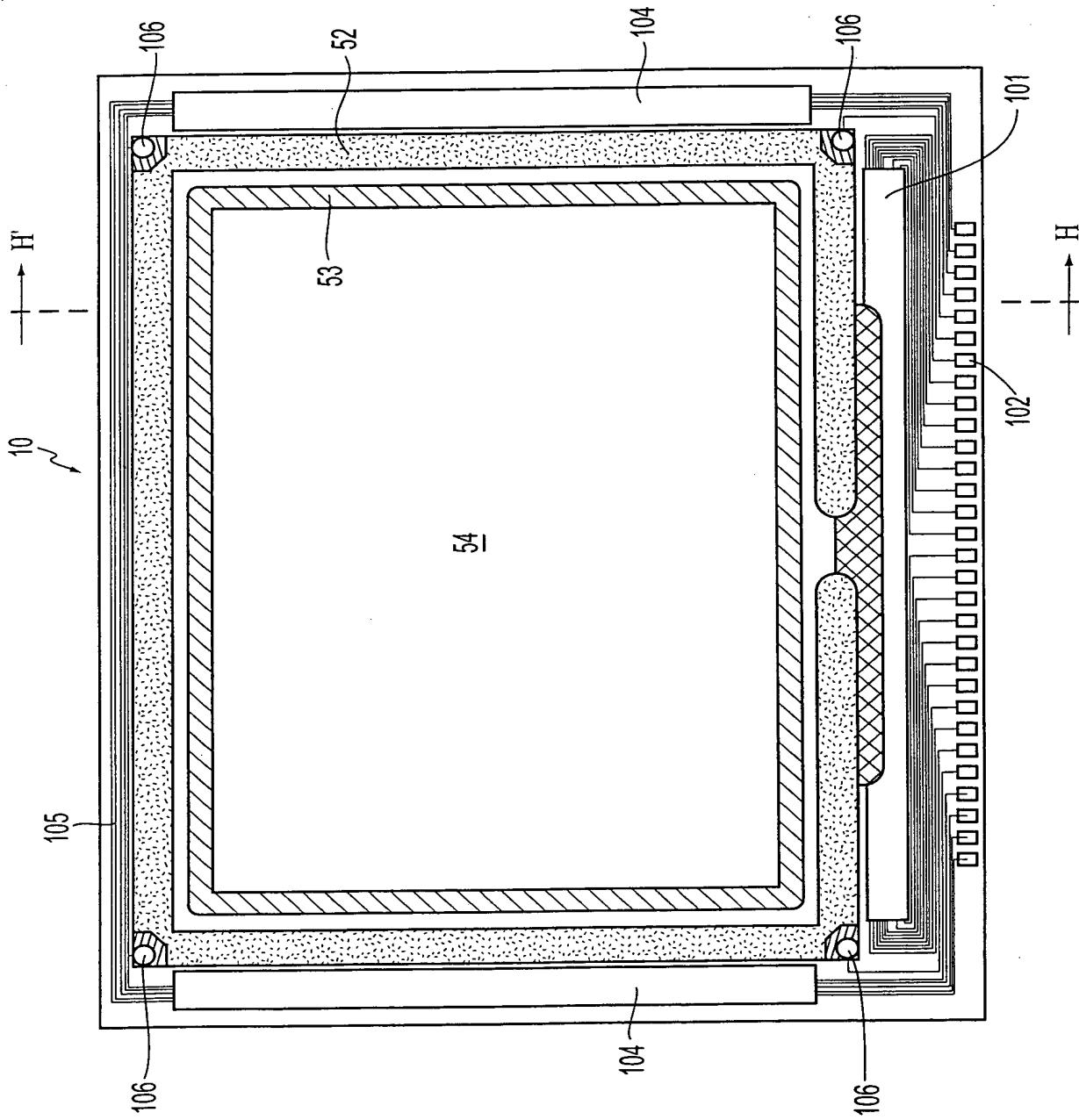


FIG. 15

FIG. 16



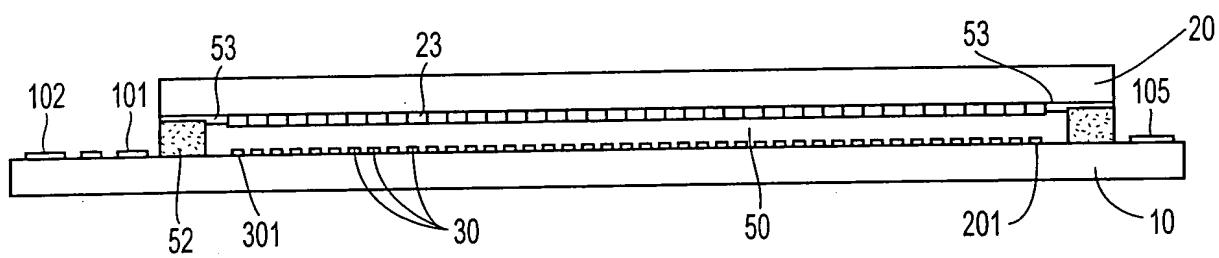


FIG. 17

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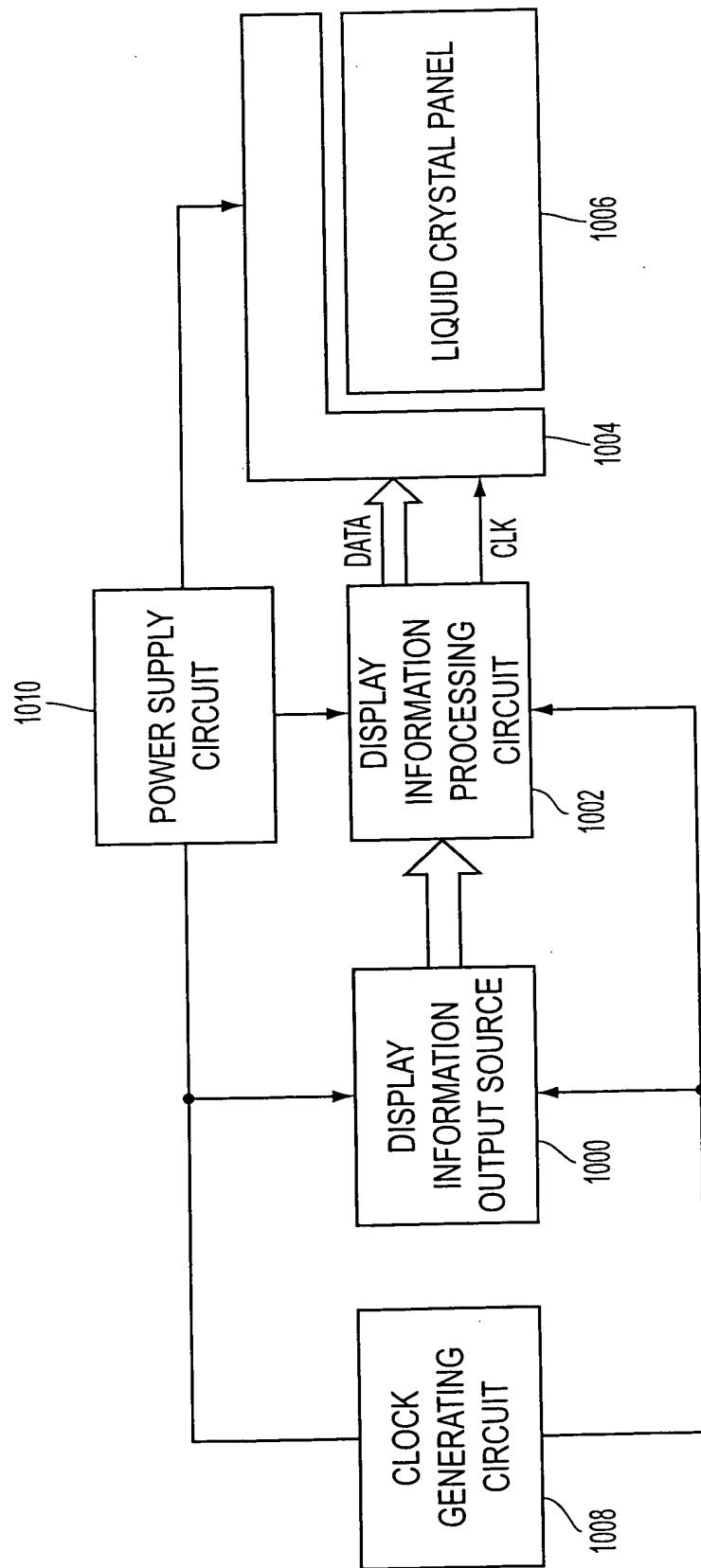


FIG. 18

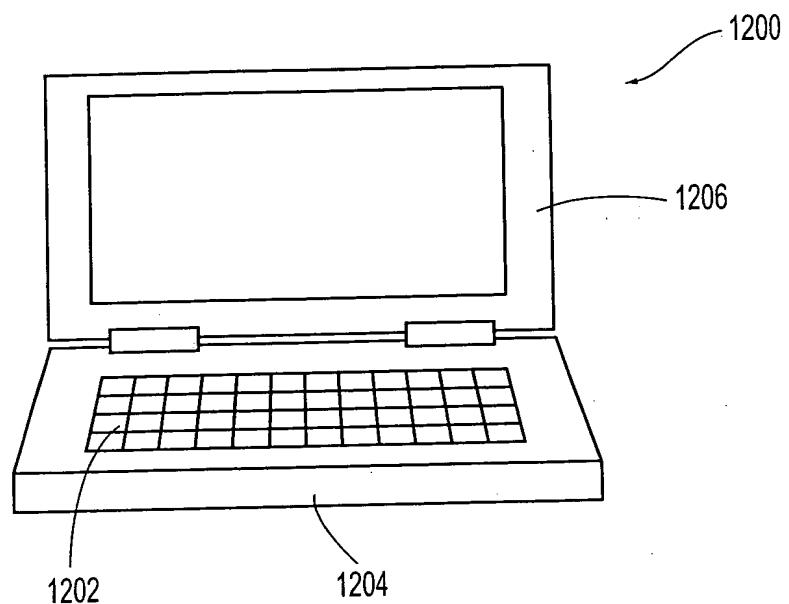


FIG. 19

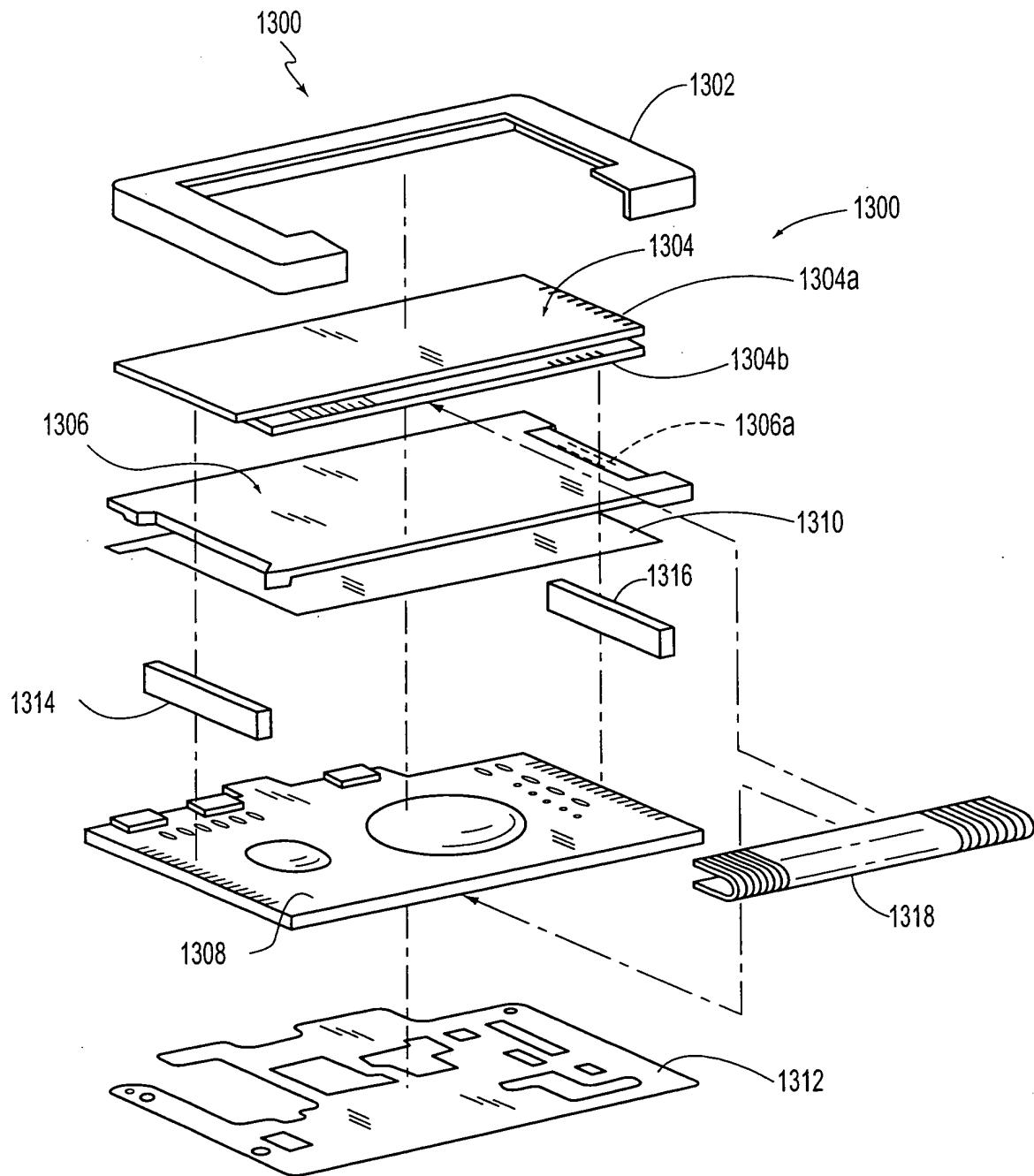


FIG. 20

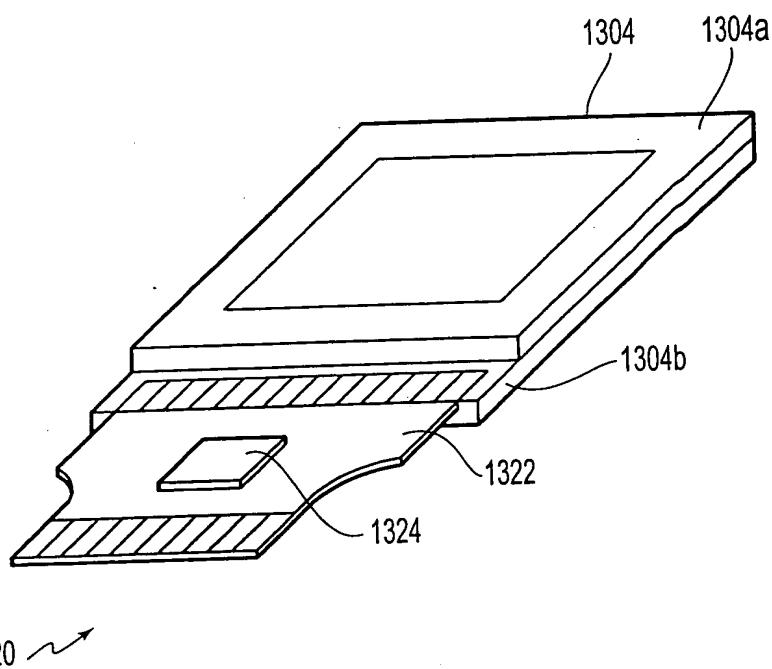


FIG. 21

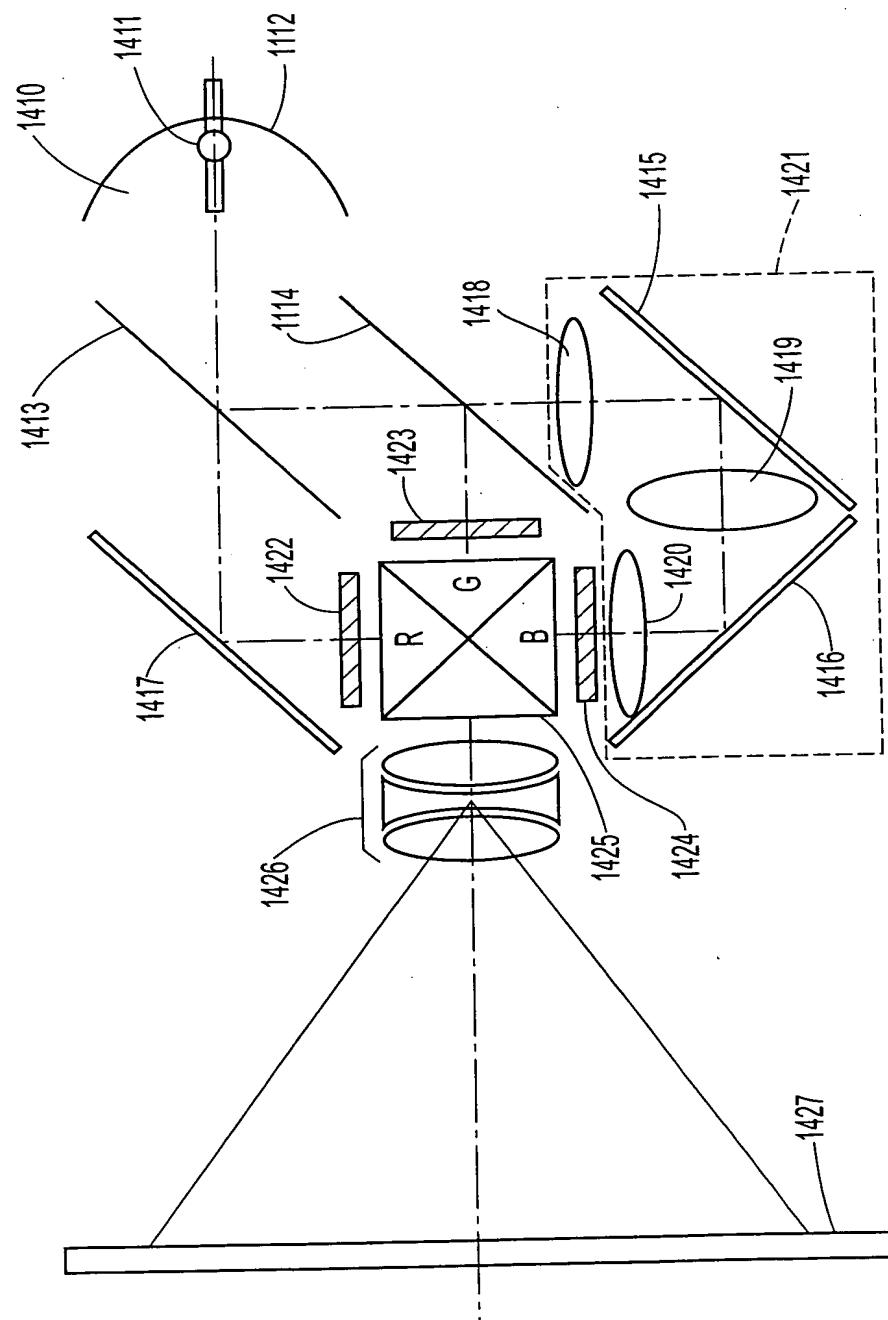


FIG. 22